

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

Integrated Redundancy Architecture and Method for Providing
Redundancy Allocation to an Embedded Memory System

Submission Type : Utility Patent Filing

Application Number:

10/707797



EFS ID:

53621

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	1796
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font format.

First Named Applicant: Wayne Ellis

Attorney Docket Number: BUR920030151US1

Timestamp: 2004-01-13 16:55:33 EDT

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-fee-sheet	BUR920030151US1-usfees.xml	2026	2004-01-13
us-fee-sheet	us-fee-sheet.xml	24912	2004-01-13
us-fee-sheet	us-fee-sheet.dtd	11069	2004-01-13
us-ids	BUR920030151US1-usidst.xml	1993	2004-01-13
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us-ids	us-ids.xml	12026	2004-01-13
us-assignment	BUR920030151US1-usassn.xml	1904	2004-01-13
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us-declaration	BUR920030151US1Dec4.tif	15816	2004-01-13
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application-body	application-body.dtd	49498	2004-01-13
application-body	wipo.ent	4956	2004-01-13
application-body	mathml2.dtd	54588	2004-01-13
application-body	mathml2-qname-1.mod	13225	2004-01-13
application-body	isoamsa.ent	5191	2004-01-13

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package-data	us-package-data.xsl	19263	2004-01-13
Total files size		822321	

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